

PATENT NUMBER

O.I.P.E.		PATENT DATE
A.C.O.L.A.		
SCANNED	Q.A.	

APPLICATION NO. 09/942102	CONT/PRIOR	CLASS 700	SUBCLASS	ART UNIT 2421	EXAMINER HOMER
APPLICANTS William Wheeler Matthew Adiletta		716	14	2825	
TITLE Generating a logic design					

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS	SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.			CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____				ISSUE FEE	
_____	_____ (Primary Examiner) (Date)			Amount Due	Date Paid
<input checked="" type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
_____	_____ (Legal Instruments Examiner) (Date)				

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